

Appl. No. 10/071,909

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (currently amended) A display system comprising:

a plurality of semiconductor light-emitting devices embedded in an insulation layer, each of said semiconductor light-emitting devices having a lower end surface disposed on a substrate, an exposed upper end portion and a light-emitting region provided between said lower end surface and said upper end portion, wherein a planar surface of the insulation layer is coplanar with the exposed upper end portion; and

a conductor film provided on an upper surface of said insulation layer and in contact with an upper end portion electrode on said exposed upper end portions of said semiconductor light-emitting devices;

wherein said light-emitting region is at a slant angle relative to said lower end surface.

2. (original) A display system as set forth in claim 1, wherein said insulation layer is composed of a high polymeric compound capable of forming a coating film containing one of a polyimide resin, an epoxy resin or a synthetic rubber.

3. (original) A display system as set forth in claim 1, wherein the insulation layer is composed of a glass capable of forming a coating film

4. (original) A display system as set forth in claim 1, wherein said insulation layer is composed of silicon oxide or silicon nitride formed by a chemical vapor deposition method or a sputtering method.

5. (previously amended) A display system as set forth in claim 1, wherein said insulation layer is formed by thinning said insulation layer by an etching method under an oxygen plasma atmosphere, a chemical mechanical polishing method or a combination of both a

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dry etching method under an oxygen plasma atmosphere and a chemical mechanical polishing method.

6. (previously amended) A display system as set forth in claim 1, wherein each said semiconductor light-emitting device has a primary light-emitting direction in the direction from said light-emitting region toward a lower end surface on said substrate surface, and said light-emitting region includes at least one reflective surface for directing light in said primary light-emitting direction.

7. (previously amended) A display system as set forth in claim 6, wherein each said semiconductor light-emitting device is formed in a pyramid shape or a truncated pyramid shape, and having at least one slant surface as said reflective surface.

8. (original) A display system as set forth in claim 6, wherein each said semiconductor light-emitting device is composed of a hexagonal crystal of a gallium nitride semiconductor, and comprises at least an active layer parallel to (1,-1,0,1) plane.

9. (original) A display system as set forth in claim 8, wherein each said semiconductor light-emitting device is composed of a gallium nitride semiconductor selectively crystal-grown in a hexagonal pyramid shape or a truncated hexagonal pyramid shape with (0,0,0,1) plane as said lower end surface and with (1,-1,0,1) plane and an equivalent surface as said slant surface, on a substrate surface of growth, and comprises an active layer parallel to (1,-1,0,1) plane and an equivalent surface.

10. (original) A display system as set forth in claim 1, wherein said display system is an image display system or an illumination system comprising said semiconductor light-emitting devices which emit monochromic light or a combination of a plurality of kinds of said semiconductor light-emitting devices which emit different-colored lights.

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11. (original) A display system as set forth in claim 1, wherein each said semiconductor light-emitting device comprises a first conduction type semiconductor layer, an active layer and a second conduction type semiconductor layer sequentially laminated, said upper end portion electrode is formed at said second conduction type semiconductor layer, and said upper end portion electrode and said conductor film are formed of the same kind of metal.

12. (previously amended) A display system as set forth in claim 1, wherein each said semiconductor light-emitting device comprises a first conduction type semiconductor layer, an active layer and a second conduction type semiconductor layer sequentially laminated, said insulation layer is thinned, and said conductor film is formed on a surface formed by said second conduction type semiconductor layer exposed by said thinning said insulation layer, whereby said upper end portion electrode and said conductor film are formed as one body with each other.

13. (original) A display system as set forth in claim 1, wherein each said semiconductor light-emitting device comprises a first conduction type semiconductor layer, an active layer and a second conduction type semiconductor layer sequentially laminated, said upper end portion electrode is formed on said second conduction type semiconductor layer with a contact metal layer of a predetermined thickness therebetween, and said upper end portion electrode and said conductor film are formed of the same kind of metal.

14. (previously amended) A display system as set forth in claim 1, wherein each said semiconductor light-emitting device comprises a first conduction type semiconductor layer, an active layer and a second conduction type semiconductor layer sequentially laminated, a contact metal layer of a predetermined thickness is formed on said second conduction type semiconductor layer, said insulation layer is thinned, and said conductor film is formed on a surface formed by said contact metal layer exposed by said thinning said insulation layer, whereby said upper end portion electrode and said conductor film are formed as one body with each other.

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15. (currently amended): A method of producing a display system by disposing and fitting a plurality of semiconductor light-emitting devices on a substrate surface, comprising the steps of:

embedding said semiconductor light-emitting devices in an insulation layer, selectively or non-selectively thinning said insulation layer to expose upper end portions of said semiconductor light-emitting devices, wherein a planar surface of the insulation layer is coplanar with the exposed upper end portion; and  
forming a conductor film on said exposed upper end portions, whereby upper end portion electrodes of said semiconductor light-emitting devices are led out to an upper surface of said insulation layer.

16. (withdrawn): A method of producing a display system as set forth in claim 15, wherein said insulation layer is formed by application of a high polymeric compound capable of forming a coating film containing an epoxy resin, a polyimide resin or a synthetic rubber.

17. (withdrawn): A method of producing a display system as set forth in claim 15, wherein said insulation layer is formed by application of a glass capable of forming a coating film.

18. (withdrawn): A method of producing a display system as set forth in claim 15, wherein said insulation layer is formed by building up silicon oxide or silicon nitride by a chemical vapor deposition method or a sputtering method.

19. (withdrawn): A method of producing a display system as set forth in claim 15, wherein said insulation layer is thinned by a dry etching method under an oxygen plasma atmosphere, a chemical mechanical polishing method or a combination of both methods.

20. (withdrawn): A method of producing a display system as set forth in claim 15, wherein semiconductor light-emitting devices each having a primary light-emitting direction in the direction from a light-emitting region toward a lower end surface on said substrate surface

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and comprising a reflective surface for downward reflection at a portion above said light-emitting region are used as said semiconductor light-emitting devices.

21. (withdrawn): A method of producing a display system as set forth in claim 15, wherein semiconductor light-emitting devices which are formed in a pyramid shape or a truncated pyramid shape and in each of which at least one slant surface of surfaces thereof is said reflective surface are used as said semiconductor light-emitting devices.

22. (withdrawn): A method of producing a display system as set forth in claim 15, wherein semiconductor light-emitting devices each composed of a hexagonal crystal of a gallium nitride semiconductor and having an active layer parallel to (1,-1,0,1) plane are used as said semiconductor light-emitting devices.

23. (withdrawn): A method of producing a display system as set forth in claim 18, wherein semiconductor light-emitting devices each formed of a gallium nitride semiconductor selectively crystal-grown in a hexagonal pyramid shape or a truncated hexagonal pyramid shape with (0,0,0,1) plane as said lower end surface and with (1,-1,0,1) plane and equivalent surfaces as said slant surfaces, on a substrate surface of growth, and comprising active layers parallel to (1,-1,0,1) plane and equivalent surfaces are used as said semiconductor light-emitting devices.

24. (withdrawn): A method of producing a display system as set forth in claim 15, wherein semiconductor light-emitting devices each comprising a first conduction type semiconductor layer, an active layer and a second conduction type semiconductor layer sequentially laminated, and each comprising an upper end portion electrode formed at said second conduction type semiconductor layer are used as said semiconductor light-emitting devices, and said conductor film is formed of the same kind of metal as said upper end portion electrode.

25. (withdrawn): A method of producing a display system as set forth in claim 15, wherein semiconductor light-emitting devices each comprising a first conduction type

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semiconductor layer, an active layer and a second conduction type semiconductor layer sequentially laminated are used as said semiconductor light-emitting devices, said insulation layer is thinned, and said conductor film is formed on a surface formed by said second conduction type semiconductor layer exposed by said thinning and said insulation layer, whereby said upper end portion electrode and said conductor film are formed as one body with each other.

26. (withdrawn): A method of producing a display system as set forth in claim 15, wherein semiconductor light-emitting devices each comprising a first conduction type semiconductor layer, an active layer and a second conduction type semiconductor layer sequentially laminated, and each comprising an upper end portion electrode formed on said second conduction type semiconductor layer with a contact metal layer of a predetermined thickness therebetween are used as said semiconductor light-emitting devices, and said conductor film is formed of the same kind of metal as said upper end portion electrode.

27. (withdrawn): A method of producing a display system as set forth in claim 15, wherein semiconductor light-emitting devices each comprising a first conduction type semiconductor layer, an active layer and a second conduction type semiconductor layer sequentially laminated and each comprising a contact metal layer of a predetermined thickness are used as said semiconductor light-emitting devices, and said conductor film is formed on a surface formed by said contact metal layer exposed by thinning of said insulation layer and said insulation layer, whereby said upper end portion electrode and said conductor film are formed as one body with each other.

28. (currently amended) A display system comprising:  
at least one semiconductor light-emitting devices embedded in an insulation layer, each of said at least one semiconductor light-emitting devices having  
a lower end surface located in said insulation layer and disposed on a substrate,  
an exposed upper end portion, wherein a planar surface of the insulation layer is coplanar with the exposed upper end portion; and

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a light-emitting region provided between said lower end surface and said upper end portion; and

a conductor film provided on an upper surface of said insulation layer, said conductor film further being in contact with an upper end portion electrode on said exposed upper end portions of said semiconductor light-emitting devices;

wherein said light-emitting region is at a slant angle relative to said lower end surface.